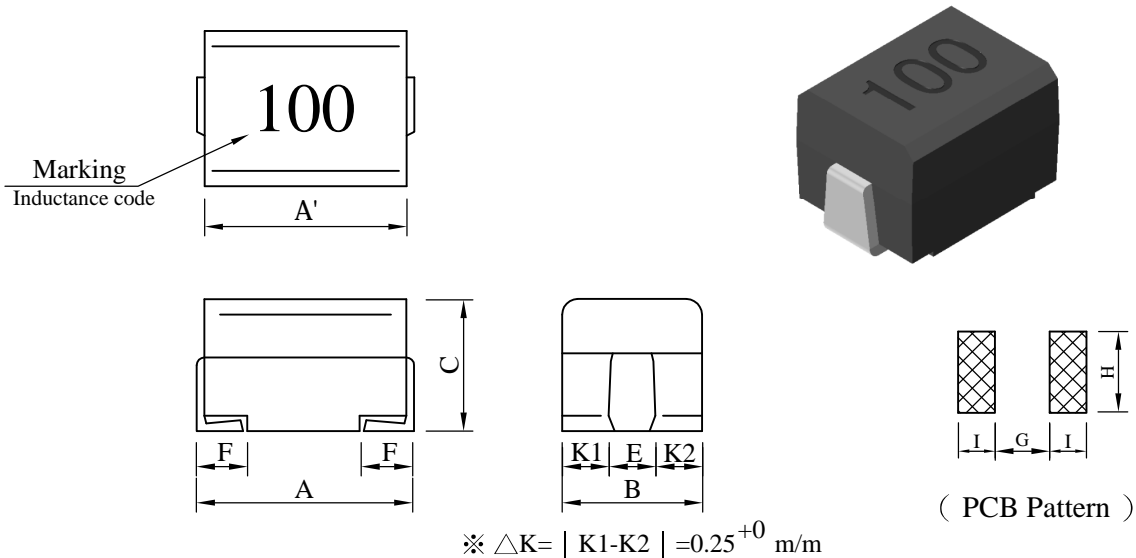


# SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Wound Chip Inductor	ABC'S DWG NO.	CC3225□□□□L□-□□□		
		REV.	20160715-M	PAGE	1

## I . Configuration and dimensions :



Unit : m/m

A	A'	B	C	E	F	G	H	I
3.20 ±0.4	2.90 ±0.2	2.50 ±0.2	2.20 ±0.2	1.00 ±0.2	0.60 $\begin{smallmatrix} +0.3 \\ -0.0 \end{smallmatrix}$	1.80	1.40	1.00

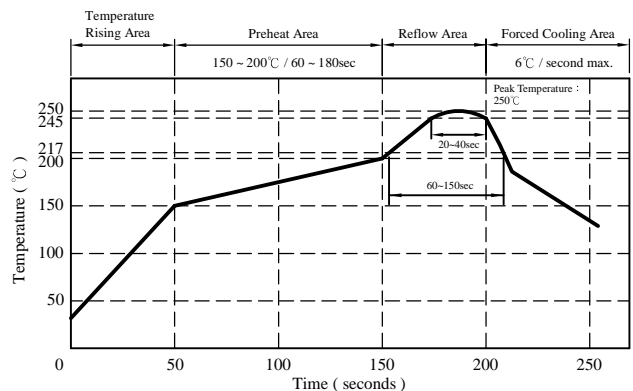
## II . Description :

- a . Ferrite drum core construction.
- b . Enamelled copper wire : H class
- c . Product weight : 0.045 g ( ref. )
- d . Moisture sensitivity Level 3
- e . Products comply with RoHS' requirements

## III . General specification :

- a . Temp. rise : 20°C max.
- b . Ambient temp. : 100°C max.
- c . Storage temp. : -40°C ----+125°C
- d . Operating temp. : -40°C ----+125°C  
(Temp. rise included)
- e . Terminal pull strength : 1.5 gf min.
- f . Rated current : Current cause  
inductance drop within 10%
- g . Resistance to solder heat : 250°C .10 secs.
- h . Resistance to solvent : Per MIL-STD-202F

Reflow profile  
 Peak Temp : 250°C max.  
 Max time above 245°C : 20~40sec max.  
 Max time above 217°C : 60~150sec max.  
 200°C~250°C Average Ramp-up Rate : 3°C/second max.



AR-001C

# SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Wound Chip Inductor	ABC'S DWG NO.	CC3225□□□□L□-□□□		
		REV.	20160715-M	PAGE	2

IV . Electrical characteristics :

DWG No.	Inductance ( $\mu$ H)	Q min.	Test Freq (MHz)	SRF (MHz) min.	RDC ( $\Omega$ ) max.	IDC (mA) max.
CC32251R0ML□-□□□	1.00±20%	10	7.96	100	0.156	770
CC32251R5ML□-□□□	1.50±20%	10	7.96	80	0.195	580
CC32252R2ML□-□□□	2.20±20%	10	7.96	65	0.260	480
CC32253R3ML□-□□□	3.30±20%	10	7.96	55	0.325	400
CC32254R7ML□-□□□	4.70±20%	10	7.96	45	0.520	320
CC32256R8ML□-□□□	6.80±20%	10	7.96	35	0.650	280
CC3225100KL□-□□□	10.00±10%	15	2.52	28	1.105	220
CC3225150KL□-□□□	15.00±10%	15	2.52	25	1.690	180
CC3225220KL□-□□□	22.00±10%	15	2.52	20	2.600	145
CC3225270KL□-□□□	27.00±10%	15	2.52	17	3.000	125
CC3225330KL□-□□□	33.00±10%	15	2.52	15	3.640	115
CC3225470KL□-□□□	47.00±10%	20	2.52	13	5.460	105
CC3225680KL□-□□□	68.00±10%	20	2.52	10	8.450	85
CC3225820KL□-□□□	82.00±10%	20	2.52	9	8.710	80
CC3225101KL□-□□□	100.00±10%	20	0.796	8	10.140	75

- 1). □ : Packaging information : □ Code
- 2). "-□□□" : Reference code
- 3). Electrical specifications at 25°C

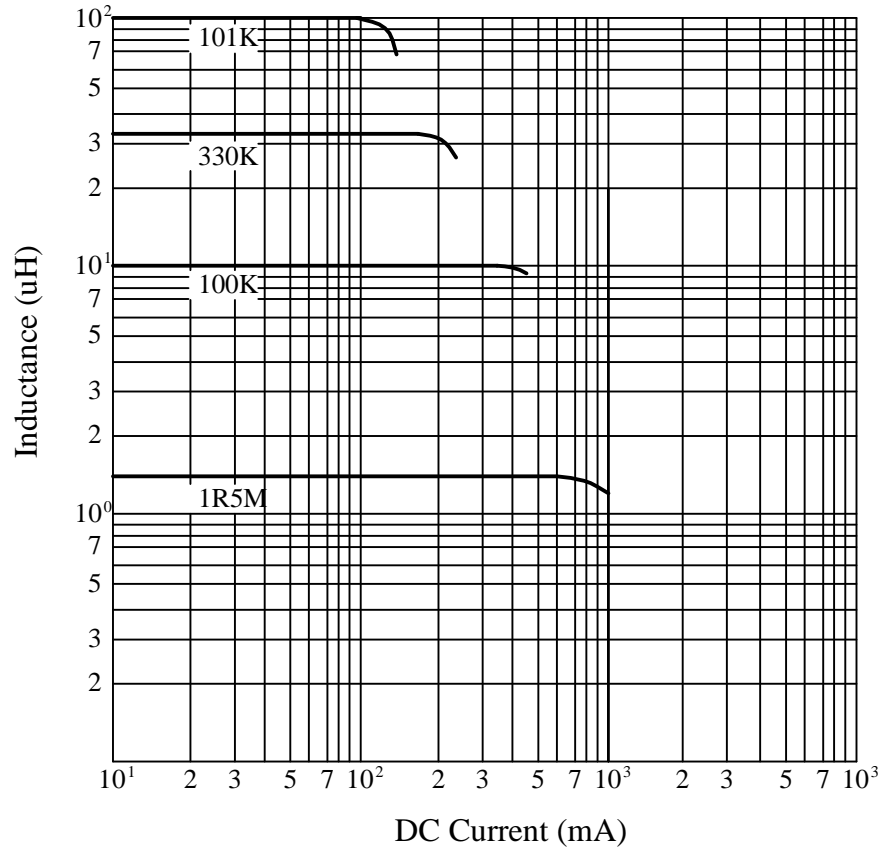
# SPECIFICATION FOR APPROVAL

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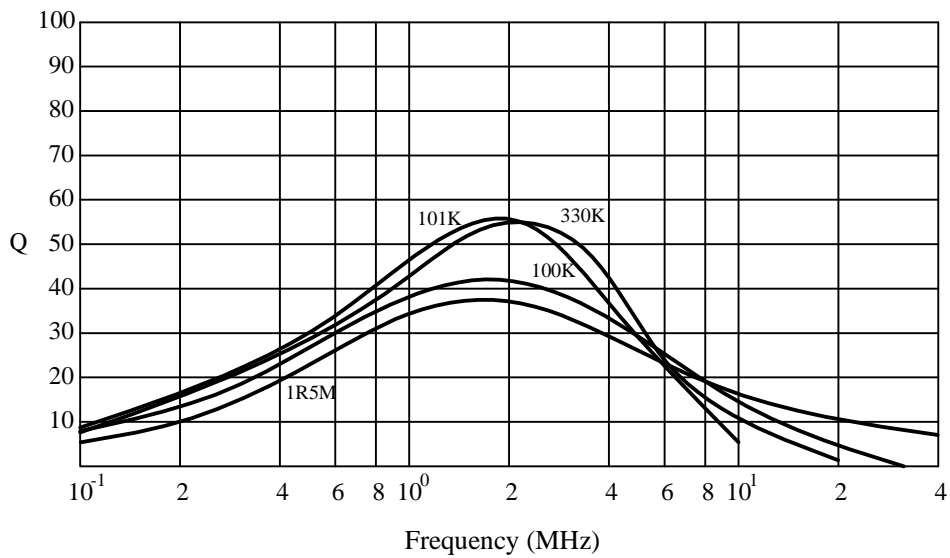
PROD. NAME	Wound Chip Inductor	ABC'S DWG NO.	CC3225□□□□L□-□□□		
		REV.	20160715-M	PAGE	3

V . Curve :

@ Inductance VS. DC Superposition Characteristics



@ Q VS. Frequency Response



AR-001C

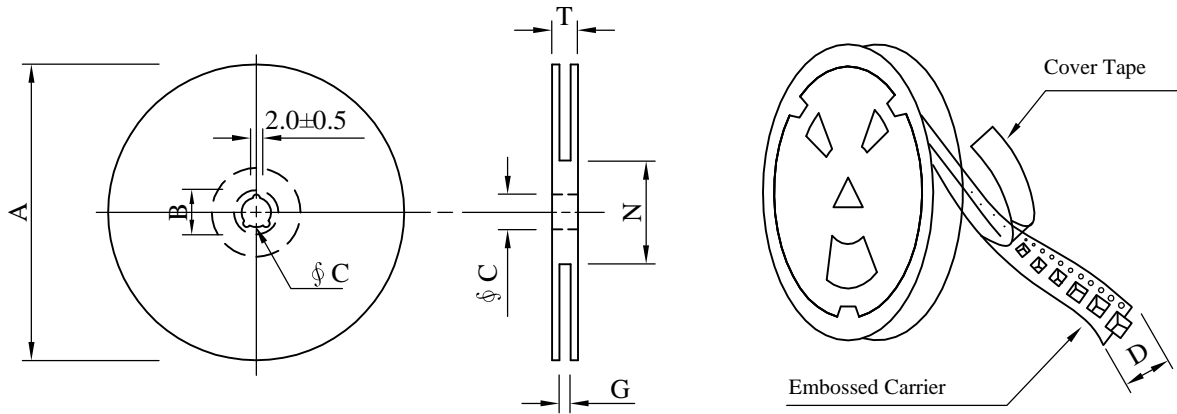
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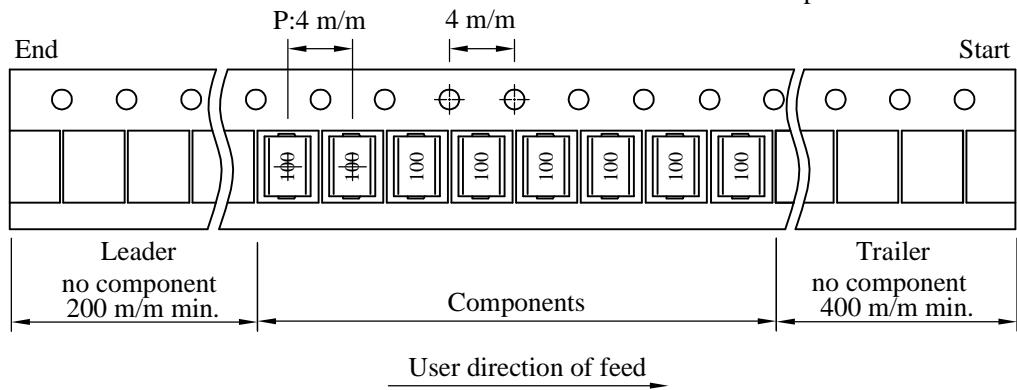
PROD. NAME	Wound Chip Inductor	ABC'S DWG NO.	CC3225□□□□L□-□□□		
		REV.	20160715-M	PAGE	4

## VI . Packaging information :

### ( 1 ) Configuration



※Carrier Tape Width : D



※ There is no differentiation or directions of polarity ( marking ) in the packaging method.

### ( 2 ) Dimensions

Unit:m/m

Style	A	B	C	D	G	N	T
07 - 08	178	21±0.8	13	8	10 <sup>+0</sup>	50 <sup>-0</sup>	12.5
07(S) - 08	183	21±0.8	13	8	10 <sup>+0</sup>	50 <sup>-0</sup>	12.5
13 - 08	330	21±0.8	13±0.5	8	10 <sup>+0</sup>	50 <sup>-0</sup>	12.5

### ( 3 ) Q'TY & G.W. Per package

Series	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (gw)	Style	Q'TY (pcs)	G.W. (Kg)	Size (cm)
B · E	1,000	110	07 - 08	50,000	7.50	41 x 39 x 22
C	2,000	220	07(S) - 08	100,000	15.00	41 x 39 x 22
D	7,000	770	13 - 08	168,000	25.20	41 x 39 x 22
F	2,000	220	07 - 08	100,000	15.00	41 x 39 x 22

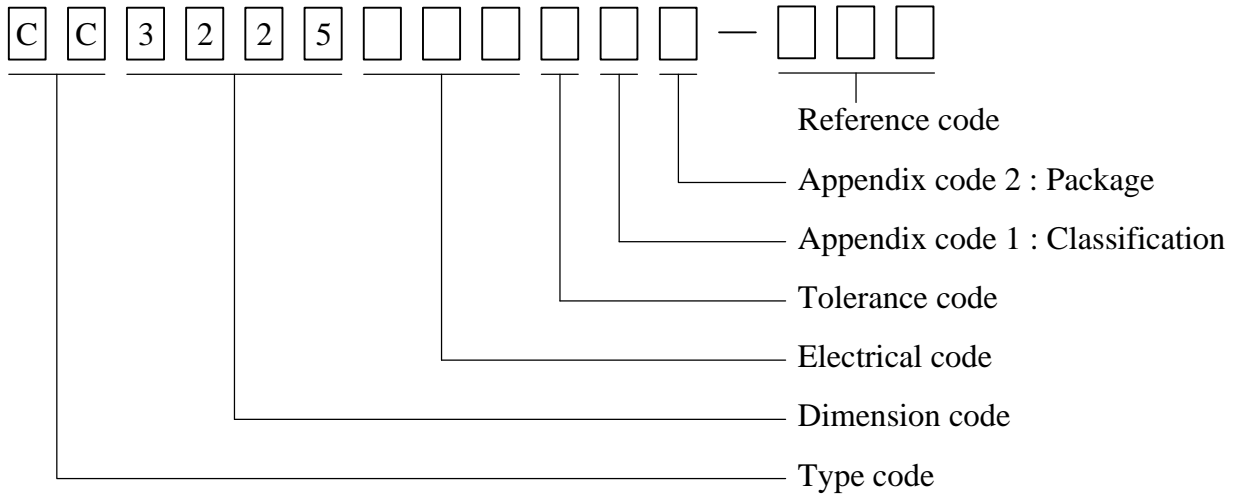
AR-001C

# SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Wound Chip Inductor	ABC'S DWG NO.	CC3225□□□□L□-□□□		
		REV.	20160715-M	PAGE	5

VI . Drawing number expression :

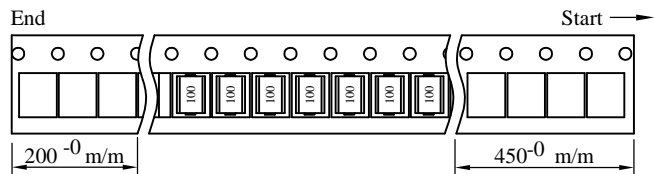


Appendix code 1 : Product Classification

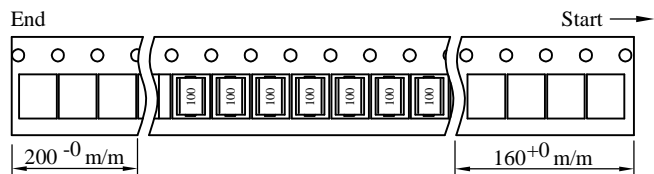
Appendix code 2 : Package Information

Code	Inner package	Cover tape	Carrier tape	Bag	Package Q'TY	Remark
B	T /R (Reel package)	Adhesive	Non-antistatic	Antistatic	1000 pcs	
C	T /R (Reel package)	Adhesive	Non-antistatic	Antistatic	2000 pcs	
D	T /R (Reel package)	Adhesive	Non-antistatic	Antistatic	7000 pcs	
E	T /R (Reel package)	Adhesive	Non-antistatic	Antistatic	1000 pcs	
F	T /R (Reel package)	Adhesive	Non-antistatic	Antistatic	2000 pcs	

Note : ① package code "B" & "C" & "E" :



Note : ② package code "F" :



AR-001C

# SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Wound Chip Inductor	ABC'S DWG NO.	CC3225□□□□L□-□□□		
		REV.	20160715-M	PAGE	6

### VIII . Reliability test :

Item	Reference documents	Test Condition	Test Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 125±2℃ 2.Time:96±2 hours.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.
2.Temperature Cycling	JESD22-A 104	1.Temperature: -40℃ ~ +125℃ 2.Number of cycle:100 cycles 3.Dwell time:30 minutes	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature : 85±2 ℃ 2.Humidity: 85% RH. 3.Time:96±2 Hours	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.
4.Operational Life	JESD22-A 108	1.Temperature: 125℃ (Temp. rise included) 2.Time:96±2 hours. 3.Rated current	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.
5.External Visual	JESD22-B 101 & MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22-B 100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for 3 cycles.	1.No body change in apperance. 2.No marking blurred. 3.Inductance shall not change more than ±10%.
8.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitud : 10-2000-10 Hz, 1.5 mm. 2.Direction:X, Y, Z 3.Test duration:2 hours for each direction, 6 hours in total.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.
9.Resistance To Soldering Heat Test	MIL-STD-202 Method 210 & J-STD020D.1	1.Highest temperature : 250±5℃. 2.Time ( temp. ≥ 217℃ ) : 60~150 Seconds. 3.IR reflow times : 3 times.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.
10.Saturation Current	JIS C 6436 & User SPEC.	1.Applied rated current for 5 seconds. 2.Rated current	Inductance shall not drop more than 10% max.
11.Over load	JIS C 6436 & User SPEC.	1.Applied one and half rated current for a period of 5 minutes. 2.Rated current	No electrical or mechanical damage
12.Temperature Rise Current	JIS C 6436 & User SPEC.	1.Applied rated current for 10 minutes. 2.Temperature measure by digital surface thermometer. 3.Irms current	Surface temperature rise is less than 20℃ max.
13.Solderability Test	J-STD-002 & JESD22-B 102	1.Baking in pre-testing : 150±5℃ / 16Hours±30 min. 2.Peak temperature : 240±5℃ 3.Time ( temp. ≥ 217℃ ) : 60~150 seconds. 4.IR reflow times : 1 time.	More than 95% soldering coverage min on terminations.
14.Electrical Characteriazation	MIL-STD-202 Method 304 & User SPEC.	1.Operating temperature : -40℃~125℃ 2.Room temperature : 25℃.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.
15.Withstanding Voltage Test	MIL-STD-202 Method 301 & User SPEC.	1.DC: 500 V (Terminal to Coating) 2.Time : 1minute	1.During the test no breakdown. 2.No mechanical or electrical damage.
16.Insulation Resistance	MIL-STD-202 Method 302	DC voltage 100V applied between inductor terminal and coating for 1 minute.	1.IR = 1000MΩ Min. 2.No mechanical or electrical damage.
17.Drop	CNS-C6354 & GB/T 2423.8	1.Products shall be mounted on SPEC. pcb and dropped down from a heigh of 1m 2.Drop total time : 6 times (Every side ofsample drop 2 times)	1. Adhesion on PCB shall be enough. 2. Product appearance shall not break. 3. No electrical damage.
18.Terminal Strength Test	IEC 60068-2-21	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.

AR-001C

